



SRAM / FLASH memory OpBn
 512Kx16 (2M lower / 2M upper bank)

\overline{WR} 1 C 22 MA14
 MA14 3 C 21 MA14 2 C 20 MA14 1 C 19 L-M31

for SRAM: 1-3 and 2-4
 for FLASH: 3-5 and 4-6

U-M29 \overline{WR} 1 C 13 MA14 2 C 12 MA14 1 C 11 U-M31

U27

A4	1A
A5	1B
A6	1C
A7	1D
A8	2A
A9	2B
A10	2C
A11	2D
A12	3A
A13	3B
A14	3C
A15	3D
A16	4A
A17	4B
A18	4C
A19	4D
A20	5A
A21	5B
A22	5C
A23	5D
A24	6A
A25	6B
A26	6C
A27	6D
A28	7A
A29	7B
A30	7C
A31	7D
A32	8A
A33	8B
A34	8C
A35	8D
A36	9A
A37	9B
A38	9C
A39	9D
A40	10A
A41	10B
A42	10C
A43	10D
A44	11A
A45	11B
A46	11C
A47	11D
A48	12A
A49	12B
A50	12C
A51	12D
A52	13A
A53	13B
A54	13C
A55	13D
A56	14A
A57	14B
A58	14C
A59	14D
A60	15A
A61	15B
A62	15C
A63	15D
A64	16A
A65	16B
A66	16C
A67	16D
A68	17A
A69	17B
A70	17C
A71	17D
A72	18A
A73	18B
A74	18C
A75	18D
A76	19A
A77	19B
A78	19C
A79	19D
A80	20A
A81	20B
A82	20C
A83	20D
A84	21A
A85	21B
A86	21C
A87	21D
A88	22A
A89	22B
A90	22C
A91	22D
A92	23A
A93	23B
A94	23C
A95	23D
A96	24A
A97	24B
A98	24C
A99	24D
A100	25A
A101	25B
A102	25C
A103	25D
A104	26A
A105	26B
A106	26C
A107	26D
A108	27A
A109	27B
A110	27C
A111	27D
A112	28A
A113	28B
A114	28C
A115	28D
A116	29A
A117	29B
A118	29C
A119	29D
A120	30A
A121	30B
A122	30C
A123	30D
A124	31A
A125	31B
A126	31C
A127	31D
A128	32A
A129	32B
A130	32C
A131	32D

C0	C10	C11	C12	C13	C14	C15	C16	C17	C18	C19	C20	C21	C22	C23	C24	C25	C26	C27	C28	C29	C30	C31	C32	C33	C34	C35	C36	C37	C38	C39	VMEM
	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	22uF	

*** Intel bus signals on 823 & 825