

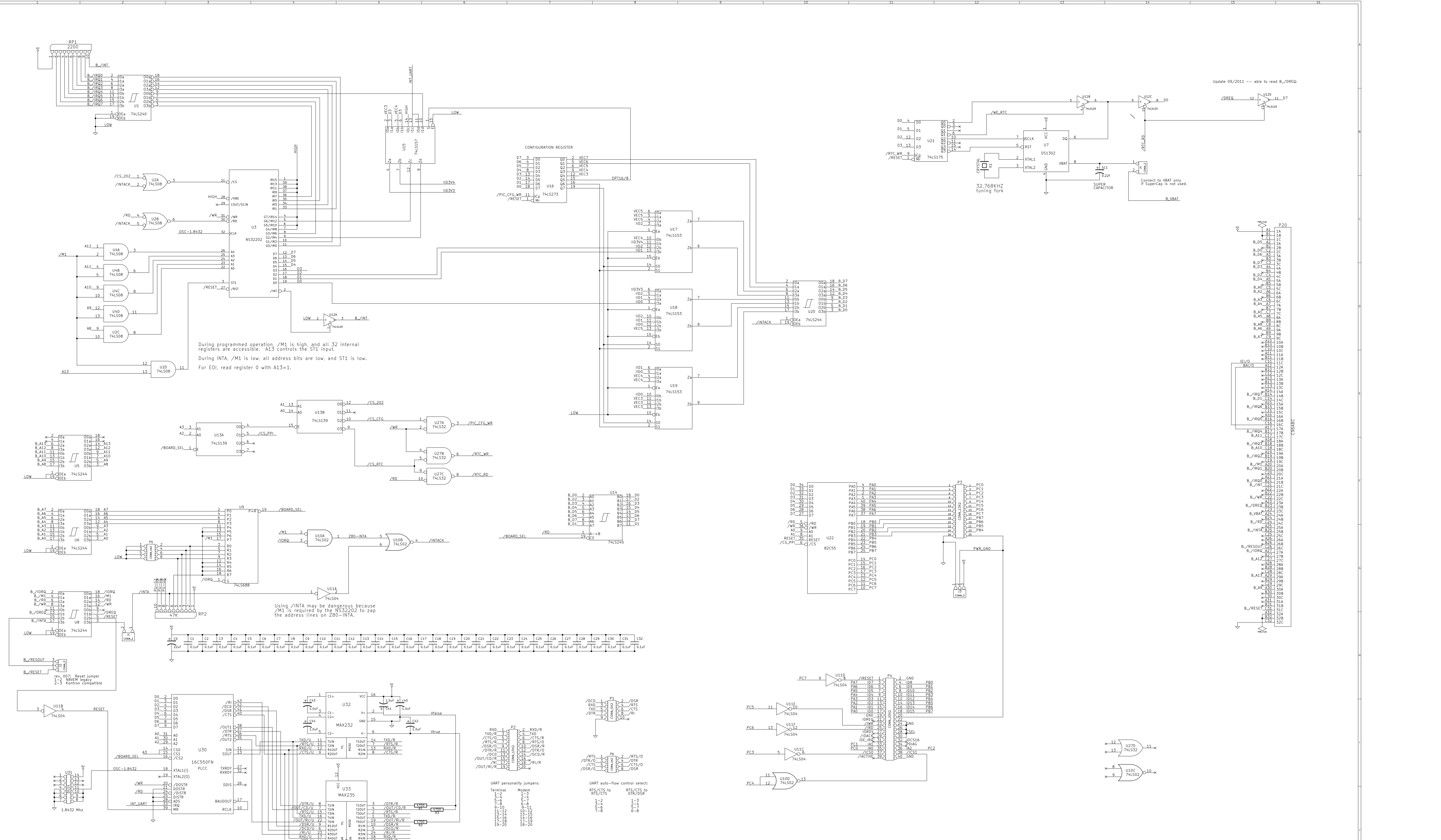
During programmed operation, /M1 is high, and all 32 internal registers are accessible. A13 controls the ST1 input. During INTA, /M1 is low, all address bits are low, and ST1 is low. For EOI, read register 0 with A13=1.

Using /INTA may be dangerous because /M1 is required by the NS32202 to zap the address lines on Z80-INTA.

UART personality jumpers:
Terminal Modem
1-2
3-4
5-6
7-8
9-10
11-12
13-14
15-16
17-18
19-20

UART auto-flow control select:
RTS/CTS to DTR/DSR
1-2
3-4
5-6

Update 09/2011 -- able to read B_/DREQ:



During programmed operation, /M1 is high, and all 32 internal registers are accessible. A13 controls the ST1 input. During INTA, /M1 is low, all address bits are low, and ST1 is low. For EOL read register 0 with A13=1.

Using /INTA may be dangerous because /M1 is required by the NS32202 to zap the address lines on Z80-INTA.

UART personality jumpers:

Terminal	Meaning
1	RxD
2	TxD
3	RxD/R
4	TxD/R
5	RTS
6	CTS
7	DTR
8	DSR
9	DCD
10	Rx/Rx
11	Tx/Tx
12	Rx/Tx
13	Rx/Rx
14	Tx/Tx
15	Rx/Rx
16	Tx/Tx
17	Rx/Rx
18	Tx/Tx
19	Rx/Rx
20	Tx/Tx

UART auto-flow control select:

RTS/CTS to	RTS/CTS to
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20

Update 09/2011 -- able to read B_/DREQ