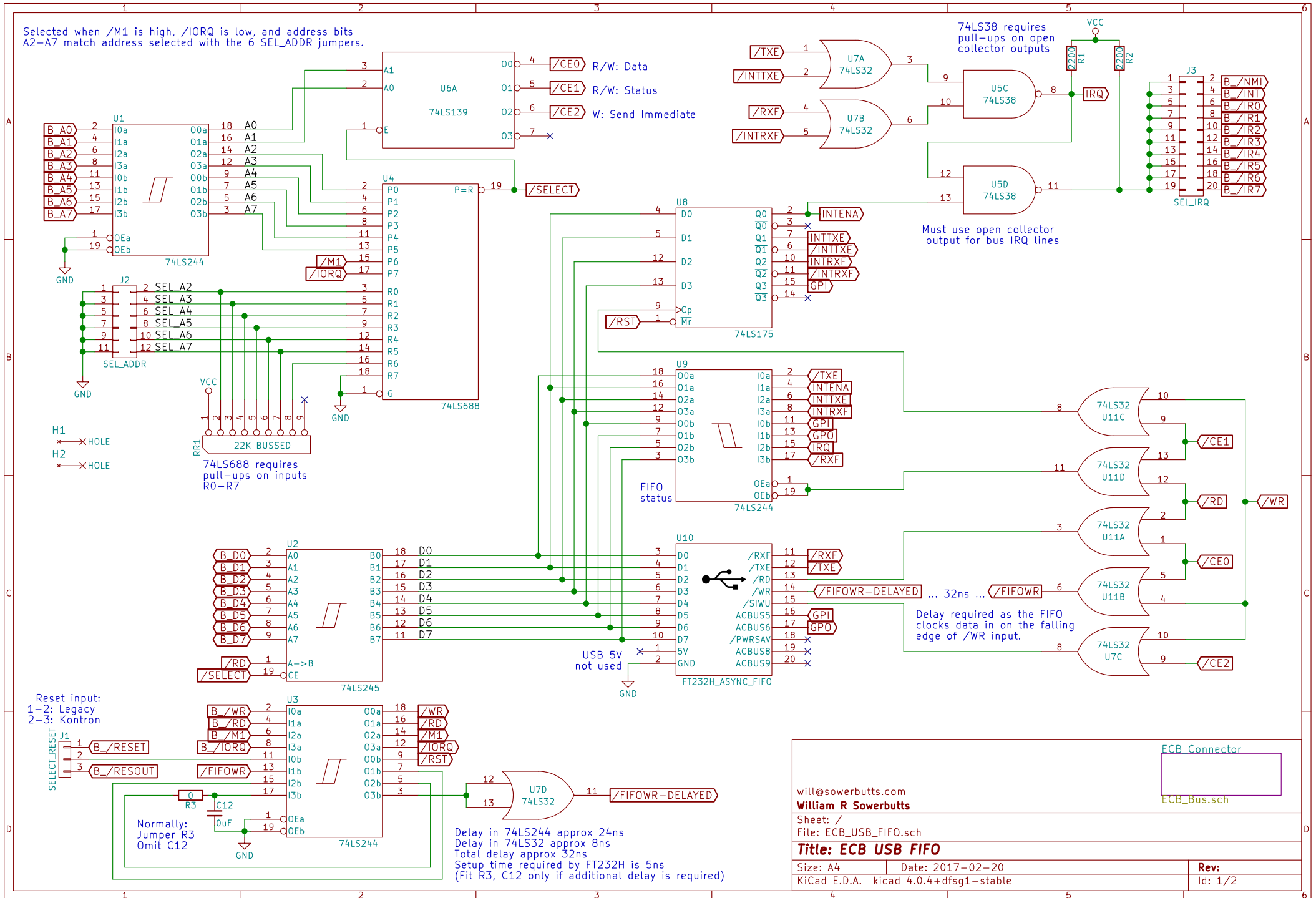


Selected when  $\overline{M1}$  is high,  $\overline{IORQ}$  is low, and address bits A2-A7 match address selected with the 6 SEL\_ADDR jumpers.



74LS38 requires pull-ups on open collector outputs

Must use open collector output for bus IRQ lines

74LS688 requires pull-ups on inputs R0-R7

Delay required as the FIFO clocks data in on the falling edge of  $\overline{WR}$  input.

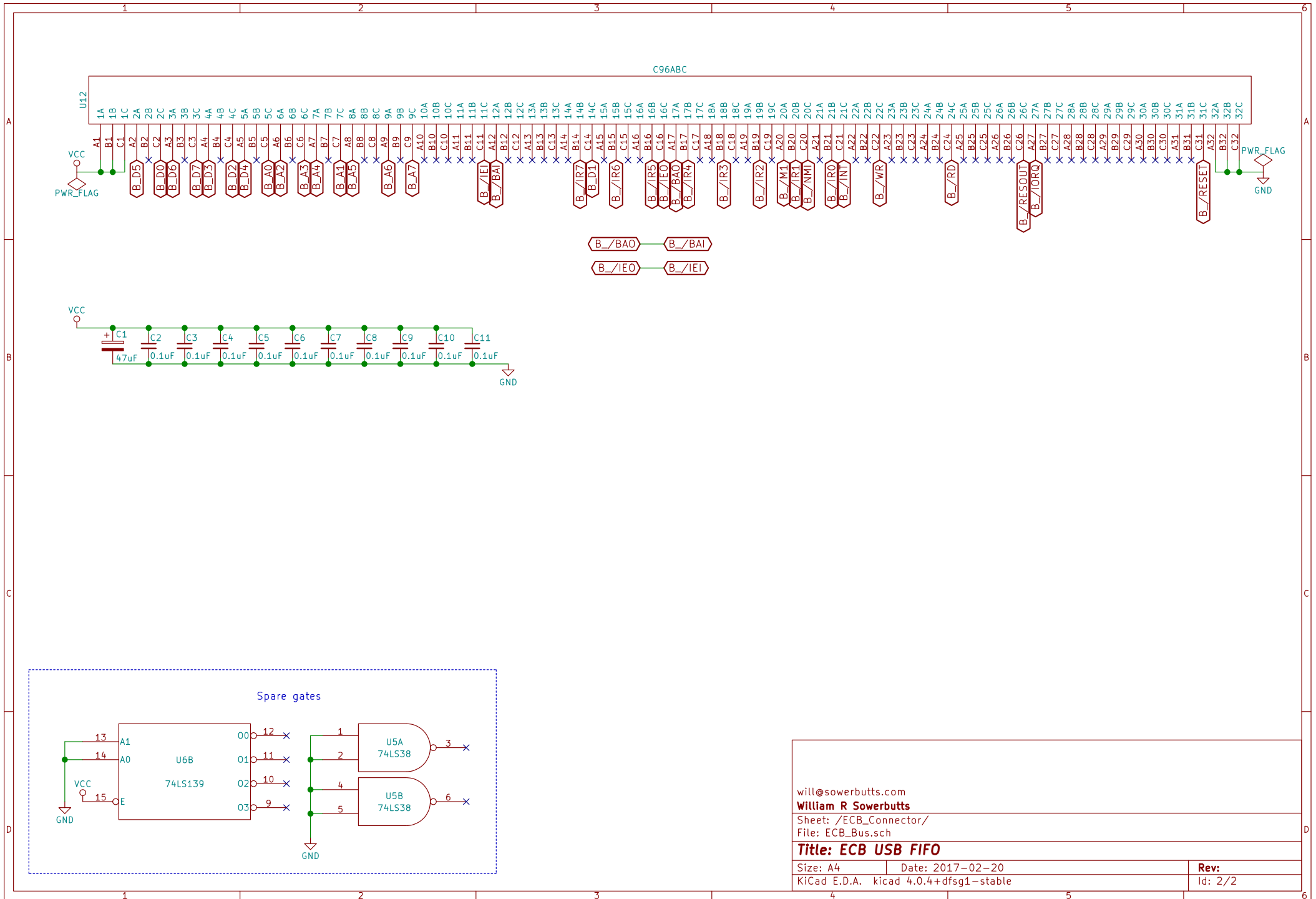
Reset input:  
1-2: Legacy  
2-3: Kontron

Normally:  
Jumper R3  
Omit C12

Delay in 74LS244 approx 24ns  
Delay in 74LS32 approx 8ns  
Total delay approx 32ns  
Setup time required by FT232H is 5ns  
(Fit R3, C12 only if additional delay is required)

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 Sheet: /  
 File: ECB\_USB\_FIFO.sch  
**Title: ECB USB FIFO**  
 Size: A4 Date: 2017-02-20  
 KiCad E.D.A. kicad 4.0.4+dfsg1-stable  
 Rev:  
 Id: 1/2

ECB\_Connector  
 ECB\_Bus.sch



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File: ECB_Bus.sch	
<b>Title: ECB USB FIFO</b>	
Size: A4	Date: 2017-02-20
KiCad E.D.A. kicad 4.0.4+dfsg1-stable	Rev: Id: 2/2