

Sheet: ECB bus



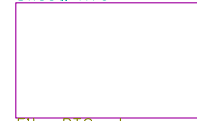
File: ECBbus.sch

Sheet: Decoder



File: Decoder.sch

Sheet: RTC



File: RTC.sch

Sheet: CPU 6502/6802



File: CPU-6802.sch

Sheet: CPU 6809



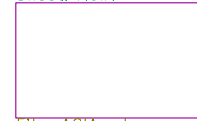
File: cpu-6809.sch

Sheet: MMU four



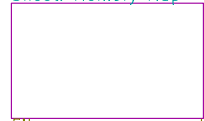
File: MMU-4.sch

Sheet: ACIA



File: ACIA.sch

Sheet: Memory Map



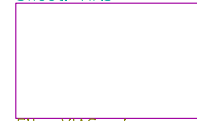
File: memory-map.sch

Sheet: IO



File: IO.sch

Sheet: VIAS



File: VIAS.sch

Sheet: PropTerm



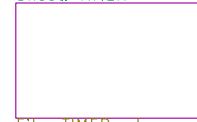
File: PropTerm.sch

Sheet: Spare



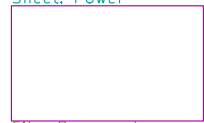
File: Spare.sch

Sheet: TIMER



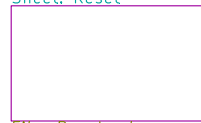
File: TIMER.sch

Sheet: Power



File: Power.sch

Sheet: Reset



File: Reset.sch

Sheet: PIA



File: PIA.sch

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N8VEM Users Group

File: 6x0x-6U.sch

Sheet: /

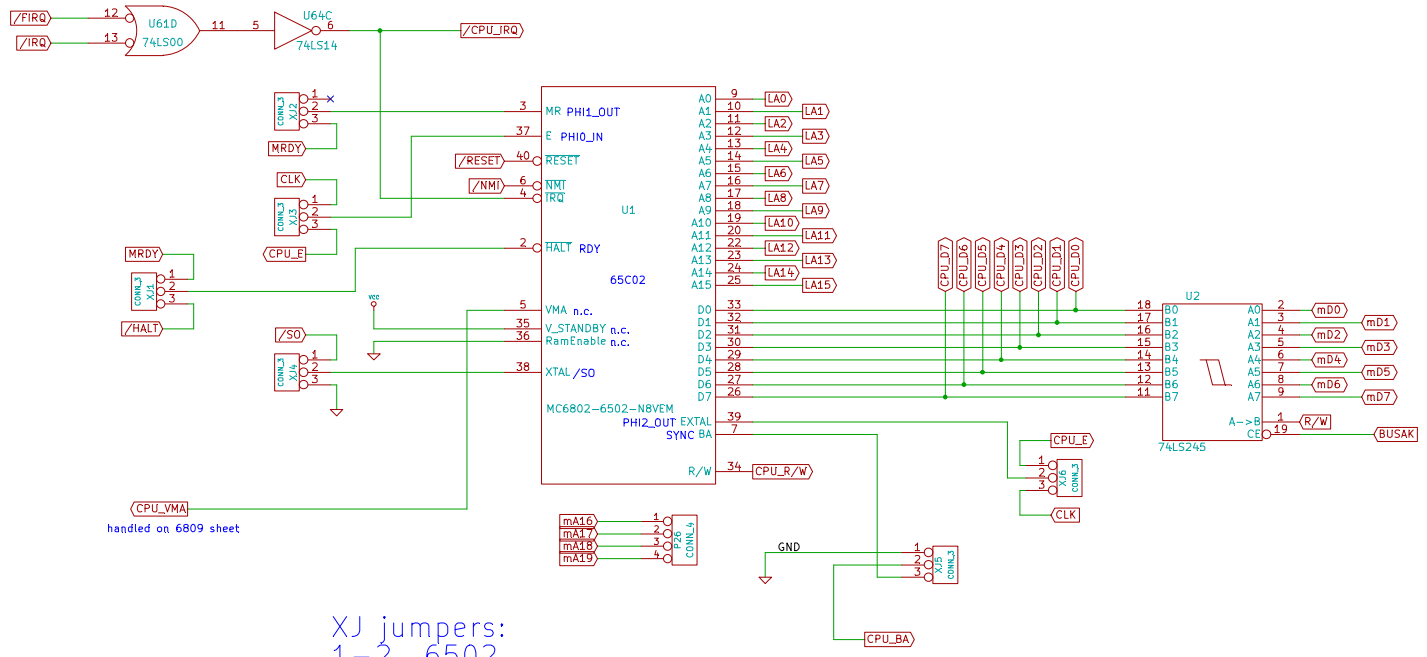
Title: 6809 and 6502 ATX and 6U

Size: A Date: 19 feb 2014

KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable

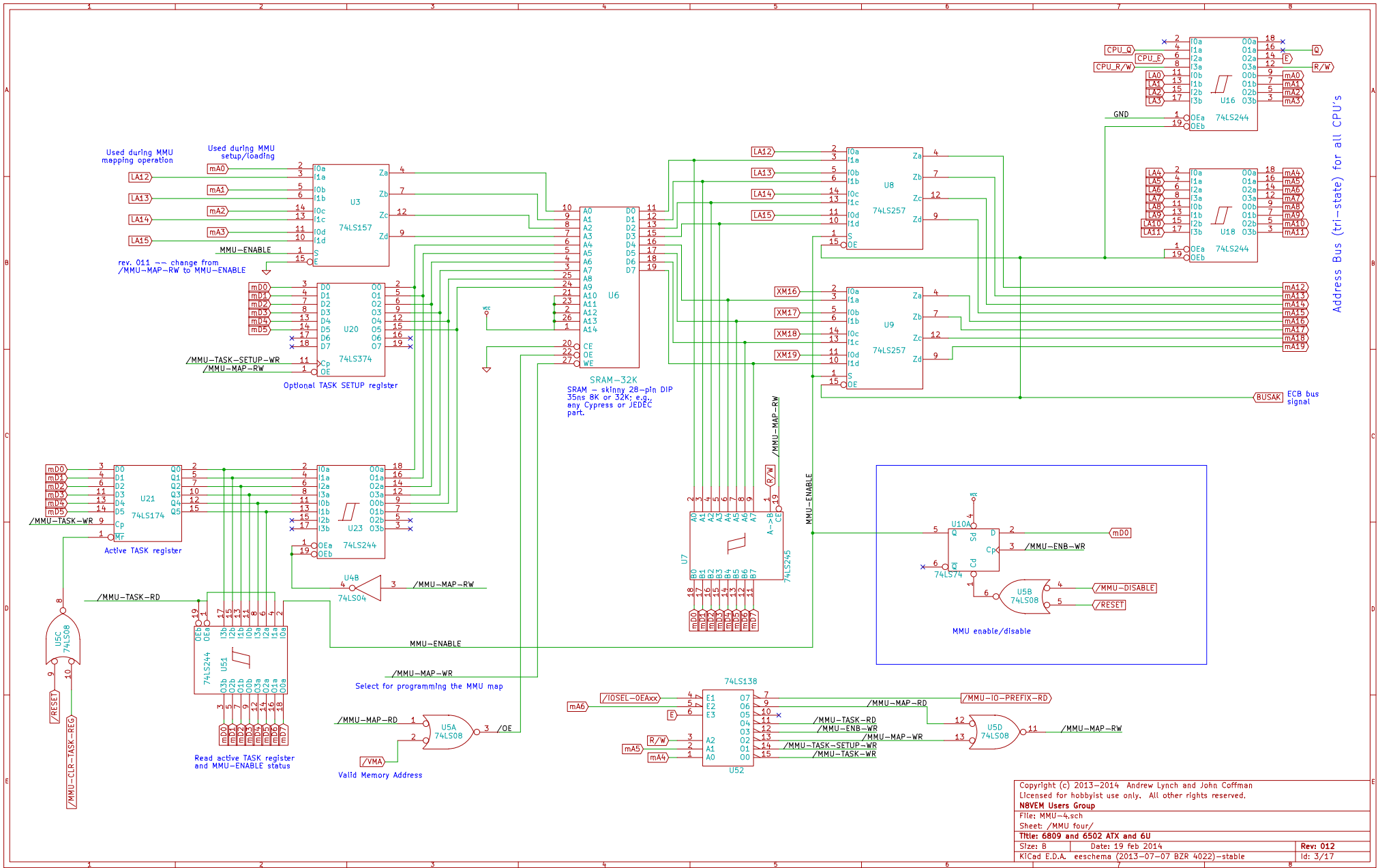
Rev: 012

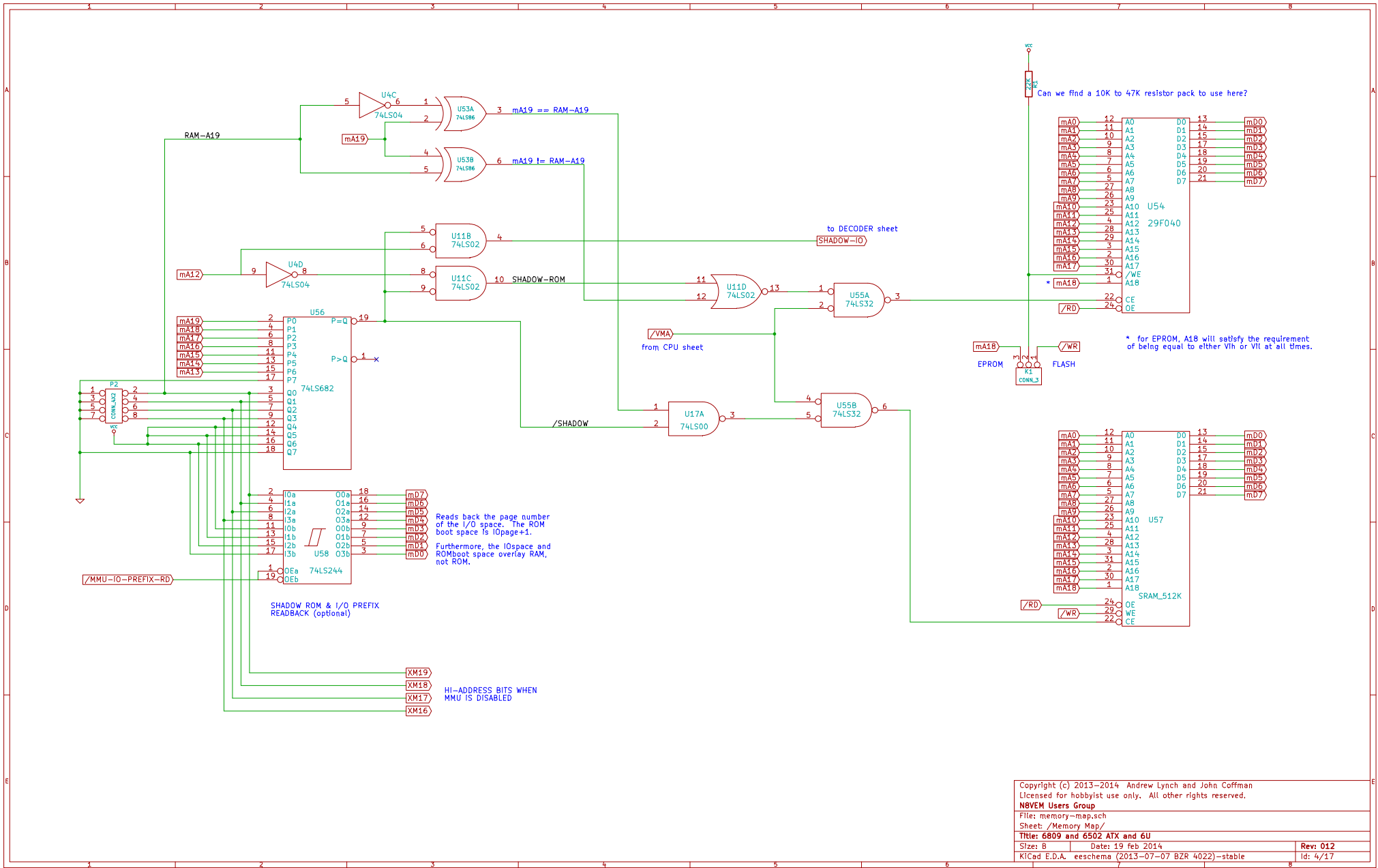
Id: 1/17

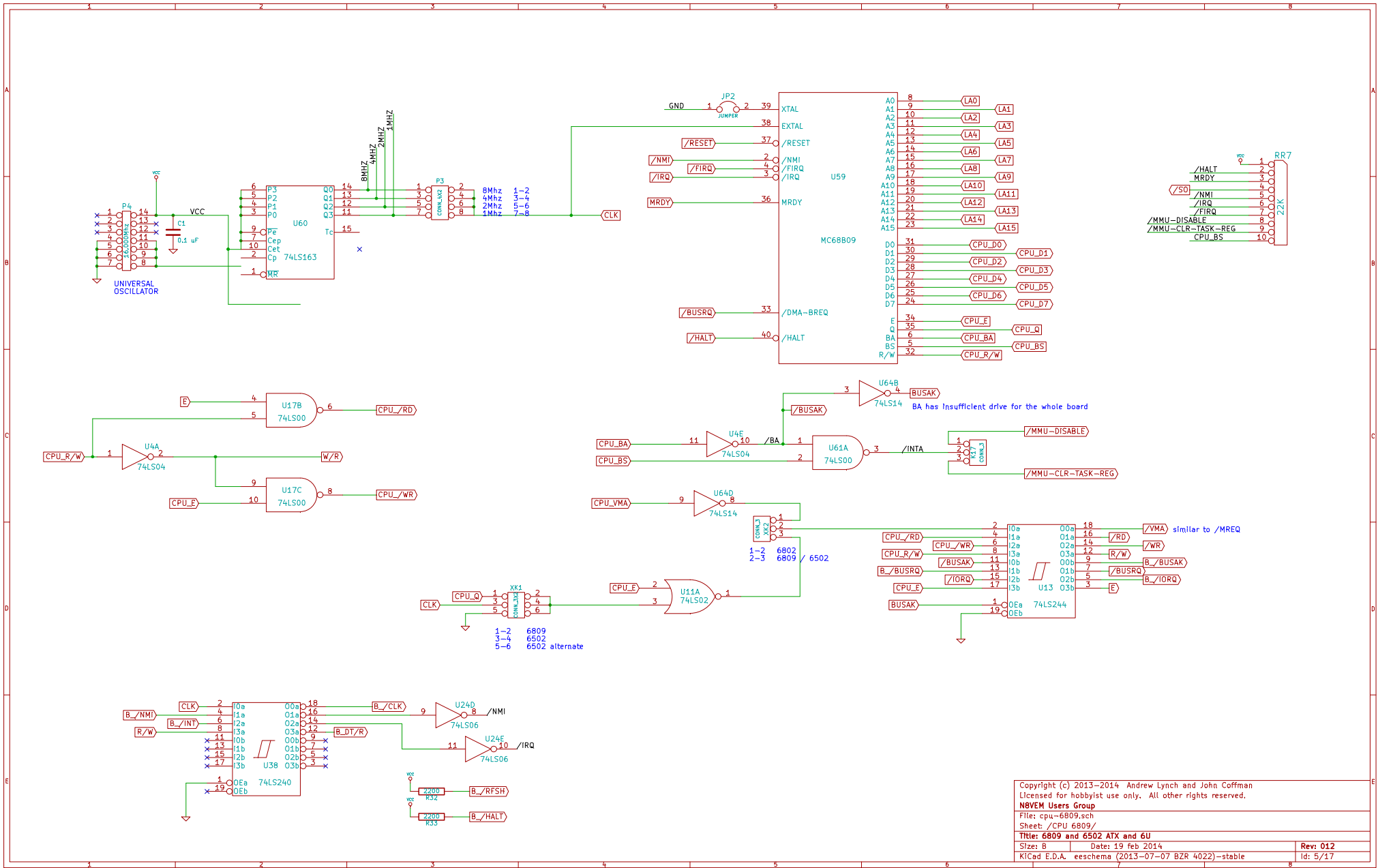


handled on 6809 sheet

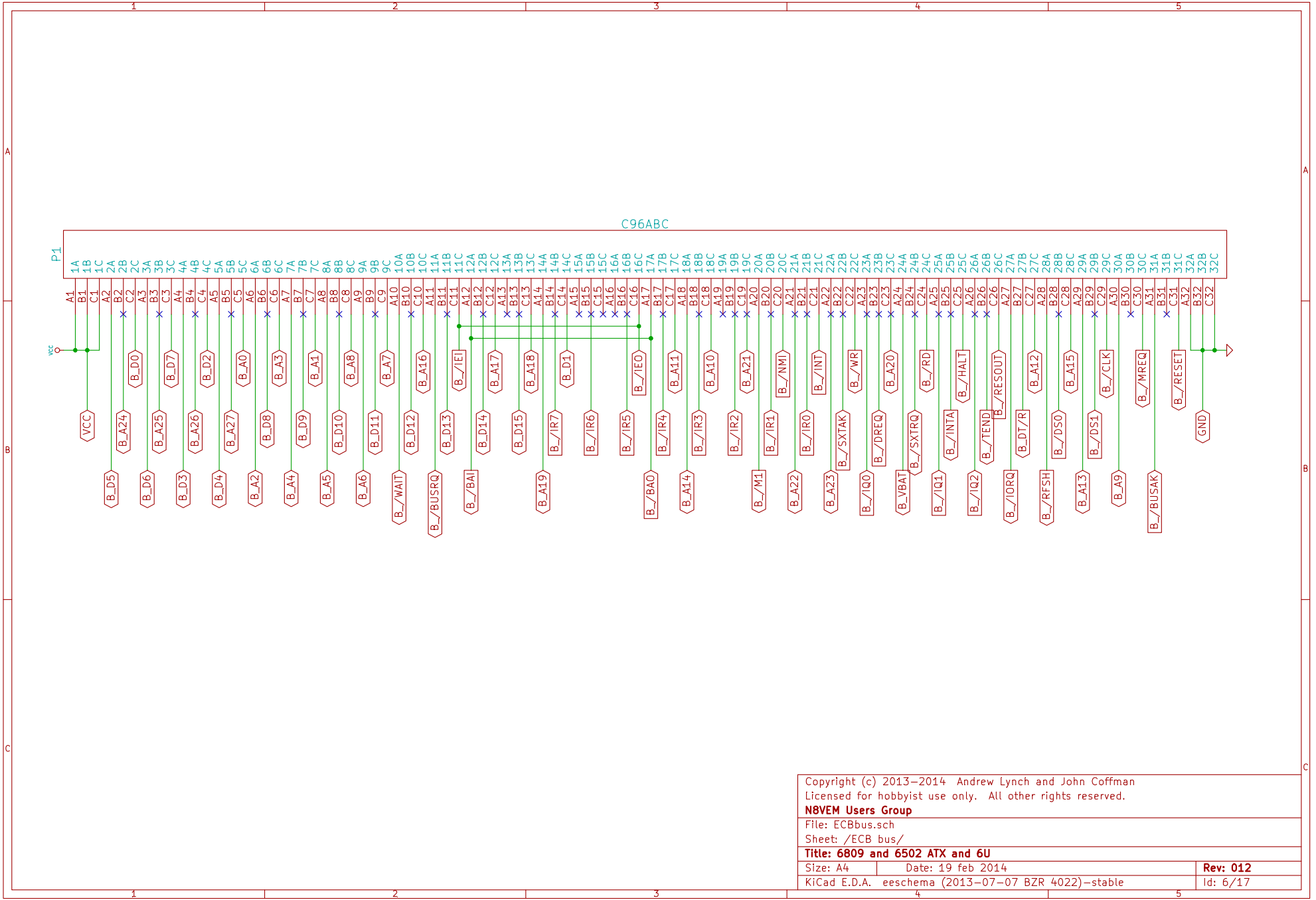
XJ jumpers:
 1-2 6502
 2-3 6802



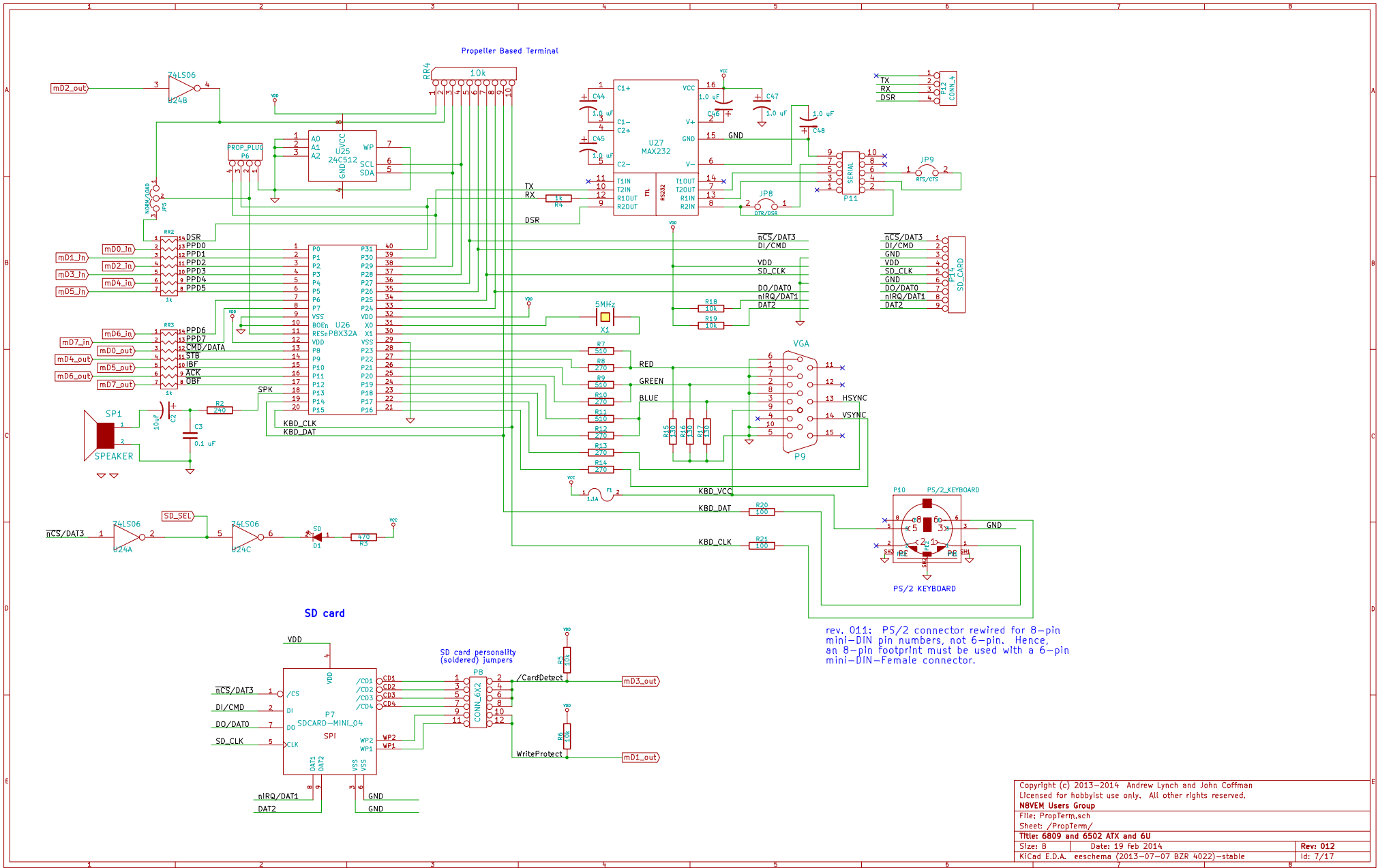




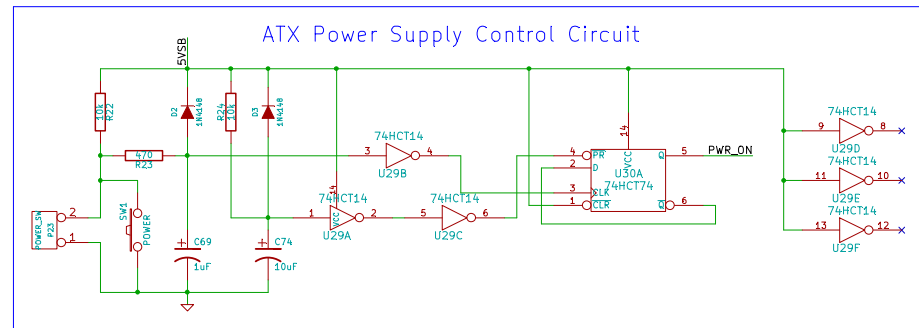
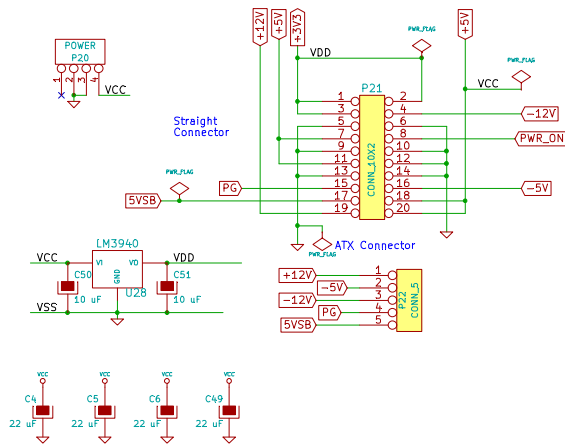
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NBVM Users Group
 File: cpu-6809.sch
 Sheet: /CPU 6809/
Title: 6809 and 6502 ATX and 6U
 Size: B Date: 19 feb 2014 Rev: 012
 KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable Id: 5/17



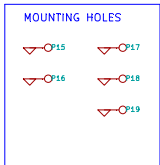
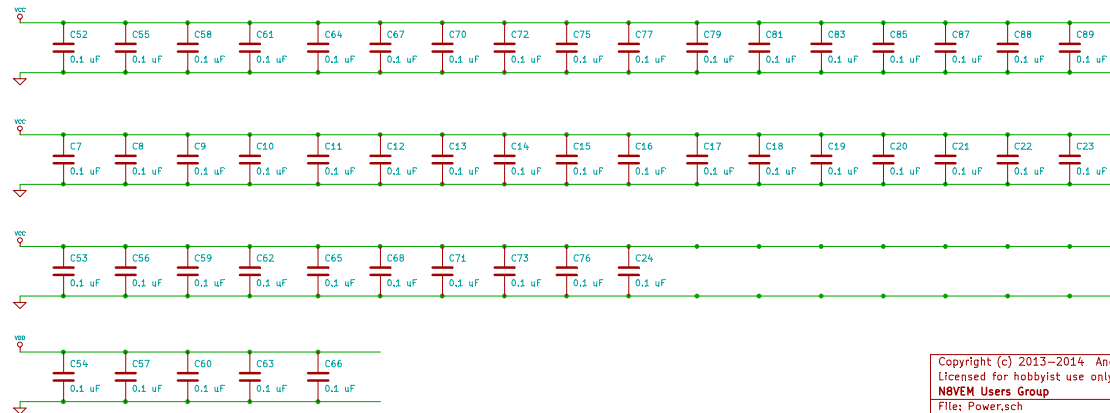
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NBVM Users Group
 File: ECBbus.sch
 Sheet: /ECB bus/
Title: 6809 and 6502 ATX and 6U
 Size: A4 Date: 19 feb 2014 **Rev: 012**
 KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable Id: 6/17



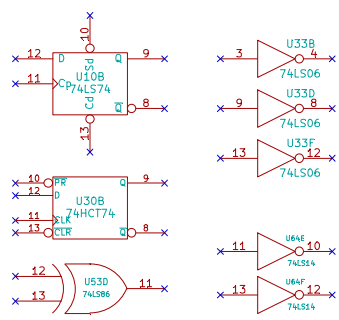
Copyright (c) 2013–2014 Andrew Lynch and John Coffman
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 File: PropTerm.sch
 Sheet: /PropTerm/
 Title: 6809 and 6502 ATX and 6U
 Size: B Date: 19 feb 2014 Rev: 012
 KiCad E.D.A. eeschema (2013–07–07 B2R 4022)–stable Id: 7/17



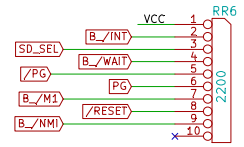
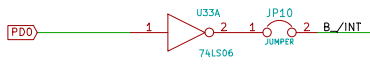
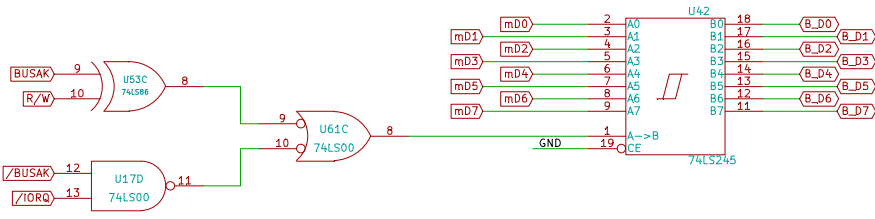
BYPASS CAPACITORS



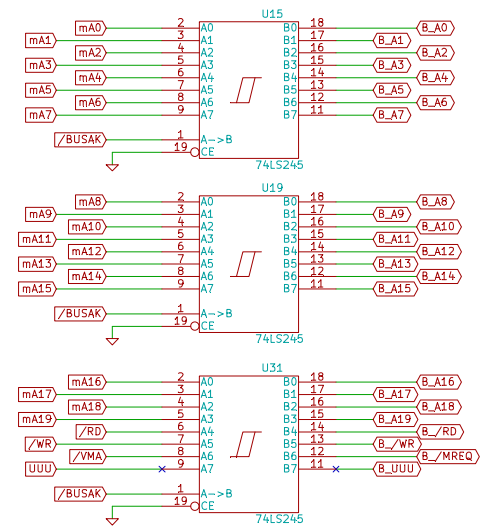
SPARE

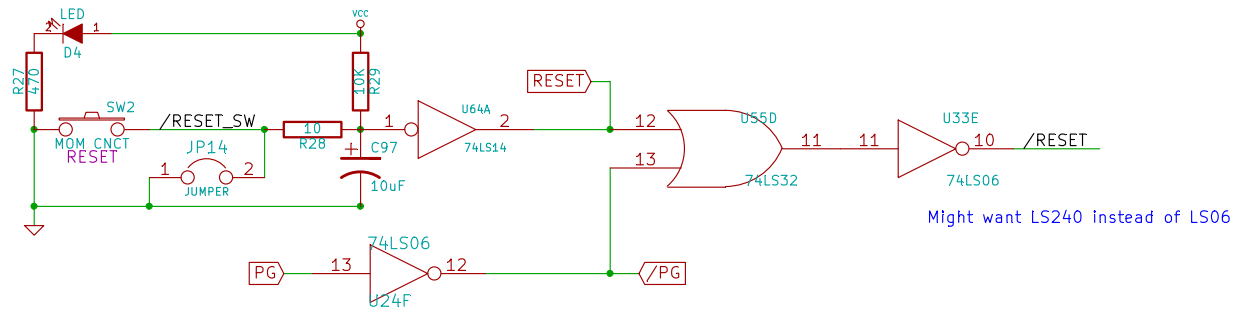


rev. 011: All signal equivalences are DELETED!!!



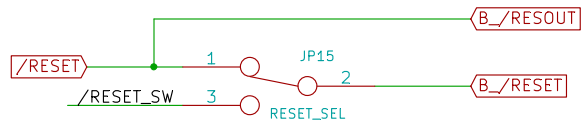
rev. 011: /RESET and /B_NMI added to RR6 pull-ups



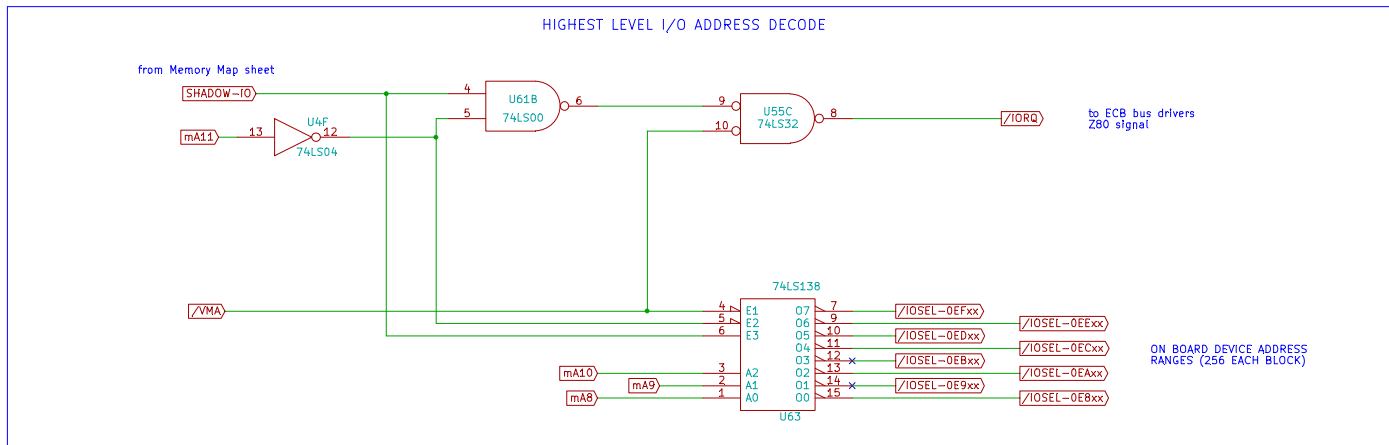


Might want LS240 instead of LS06

JP15 - Reset OUT vs. Reset sw IN
 1-2 - N8VEM (default), 2-3 - Kontron

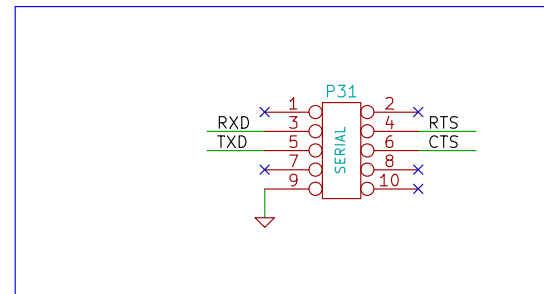
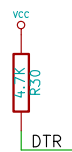
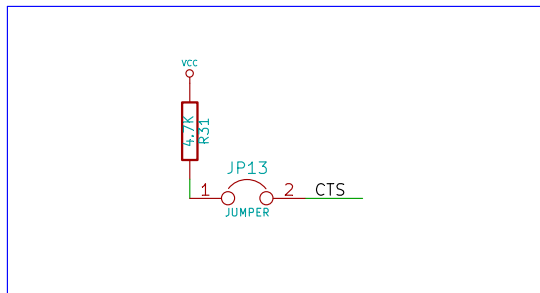
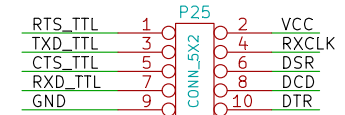
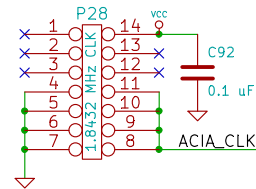
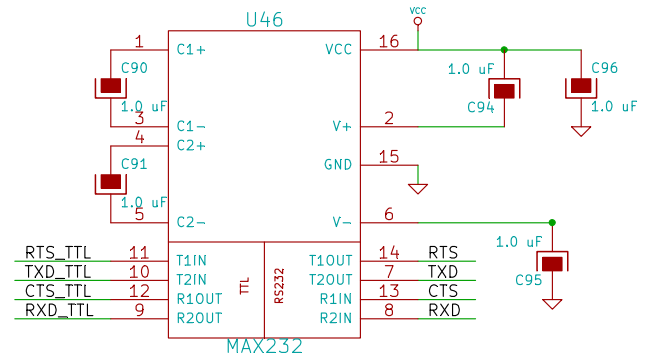
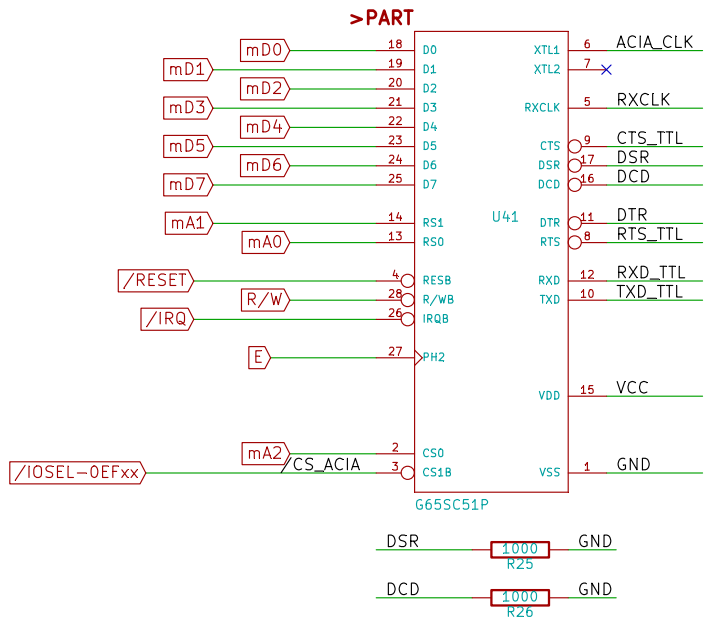


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N8VEM Users Group		
File: Reset.sch		
Sheet: /Reset/		
Title: 6809 and 6502 ATX and 6U		
Size: A	Date: 19 feb 2014	Rev: 012
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable		Id: 11/17



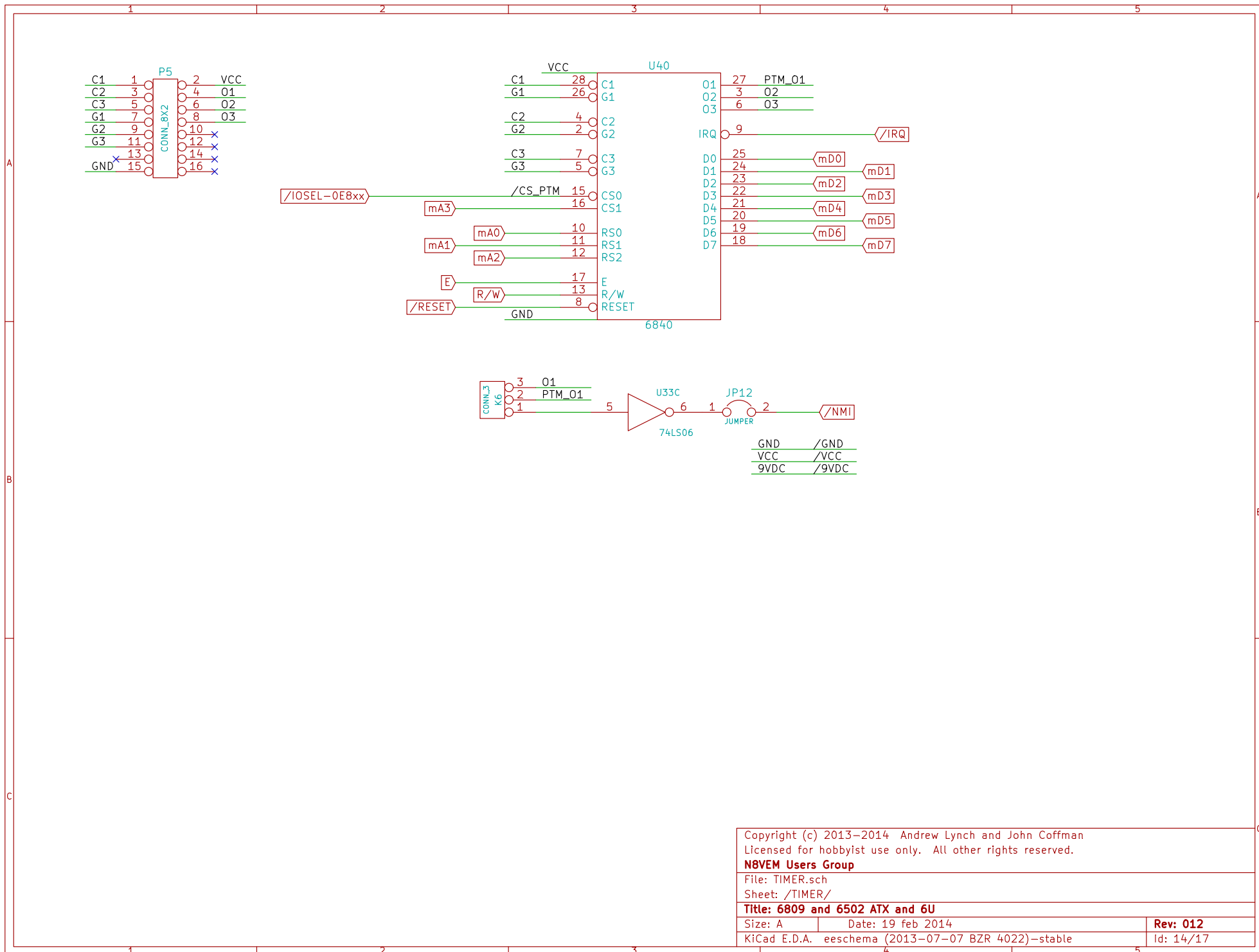
I/O DEVICE SUBSYSTEM ADDRESS ASSIGNMENTS:

\$E800 - \$E8FF	PTM
\$E900 - \$E9FF	available
\$EA00 - \$EAF7	MMU CONTROL
\$EB00 - \$EBFF	available
\$EC00 - \$ECFF	VIA0
\$ED00 - \$EDFF	VIA1
\$EE00 - \$EEFF	PIA
\$EF00 - \$EFFF	ACIA



CTS is an inverted signal on the RS-232 port. So it is really /CTS. To assert the signal, it must be tied to SPACE, which is a + RS-232 voltage. (MARK, or true, is a - RS-232 voltage.)

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N8VEM Users Group		
File: ACIA.sch		
Sheet: /ACIA/		
Title: 6809 and 6502 ATX and 6U		
Size: A	Date: 19 feb 2014	Rev: 012
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable		Id: 13/17



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N8VEM Users Group

File: TIMER.sch

Sheet: /TIMER/

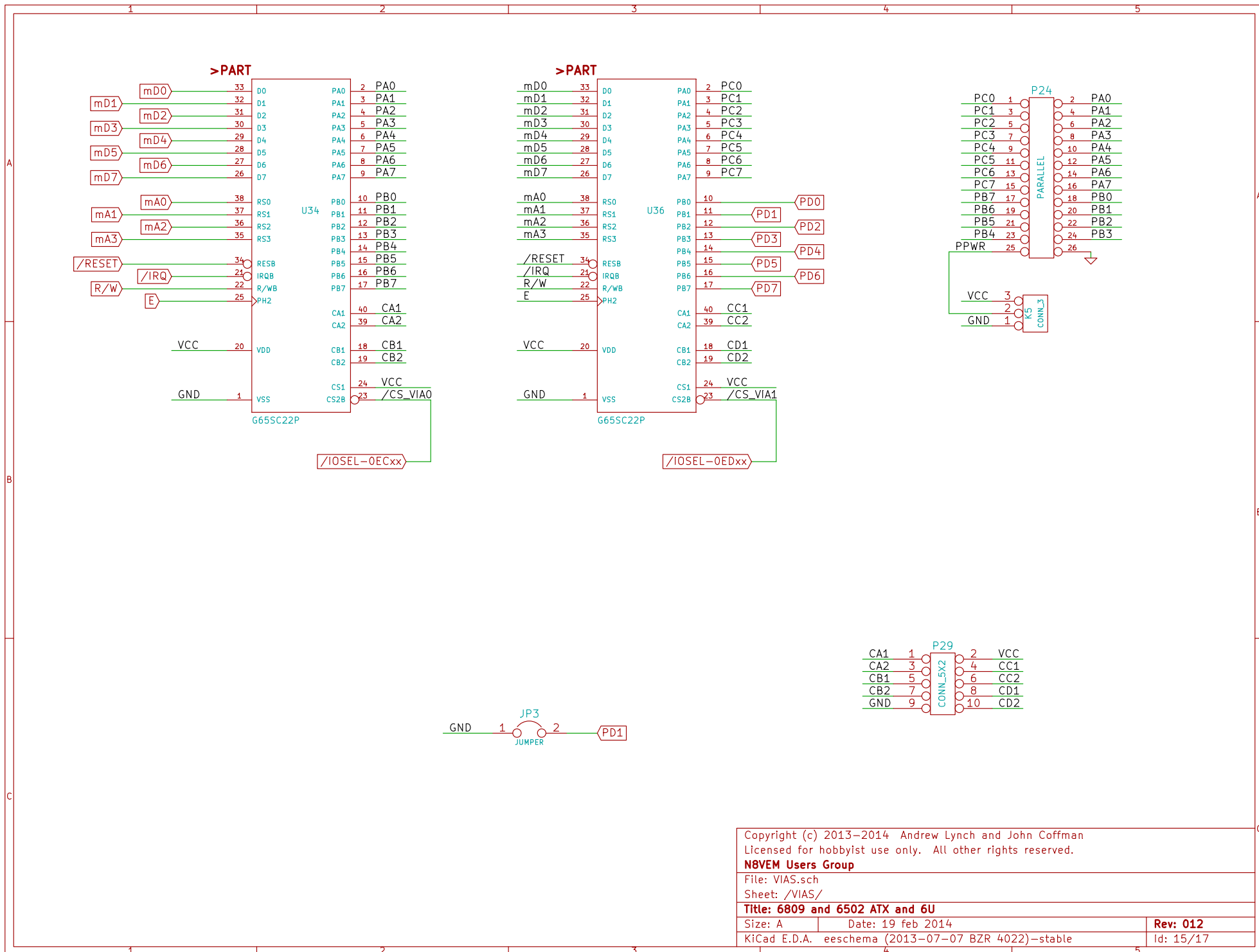
Title: 6809 and 6502 ATX and 6U

Size: A Date: 19 feb 2014

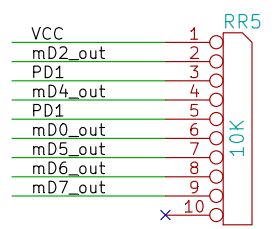
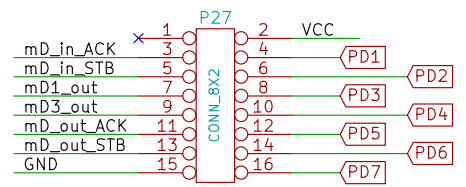
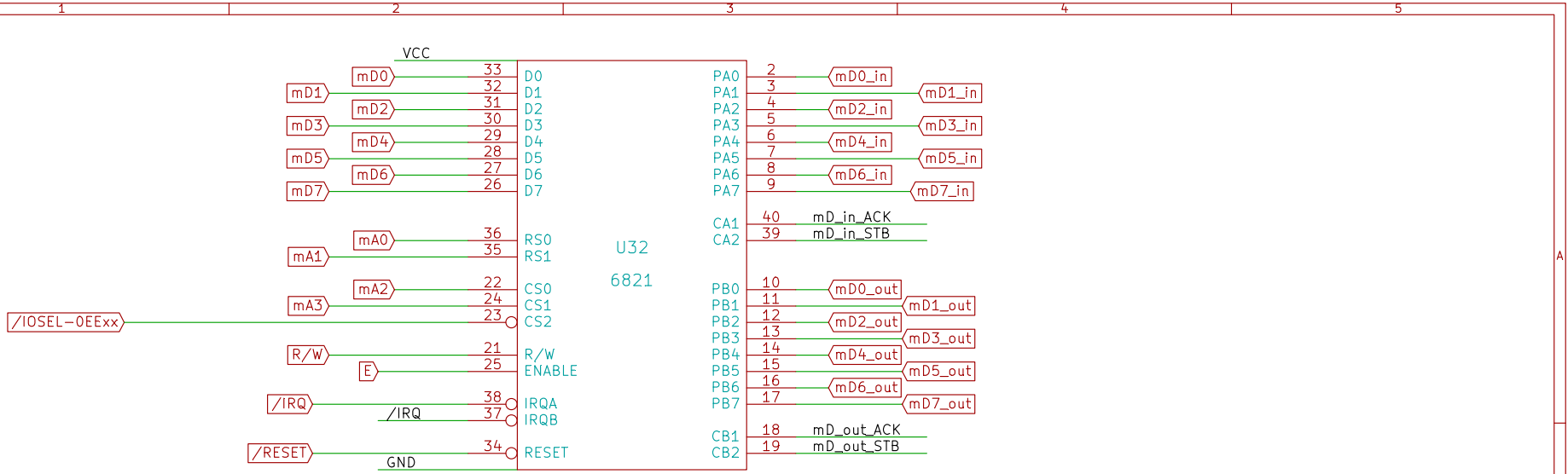
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable

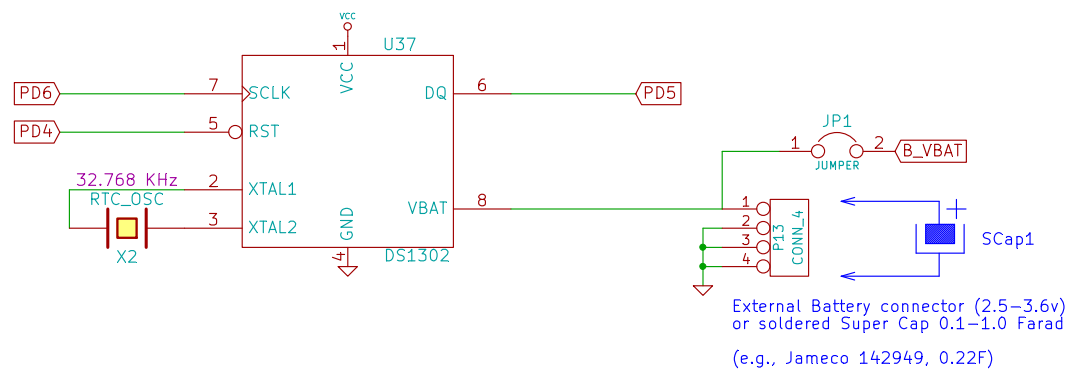
Rev: 012

Id: 14/17



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N8VEM Users Group
 File: VIAS.sch
 Sheet: /VIAS/
Title: 6809 and 6502 ATX and 6U
 Size: A Date: 19 feb 2014
 KiCad E.D.A. eschema (2013–07–07 BZR 4022)–stable **Rev: 012**
 Id: 15/17





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N8VEM Users Group

File: RTC.sch

Sheet: /RTC/

Title: 6809 and 6502 ATX and 6U

Size: A Date: 19 feb 2014

Rev: 012

KiCad E.D.A. eeschema (2013–07–07 BZR 4022)–stable

Id: 17/17