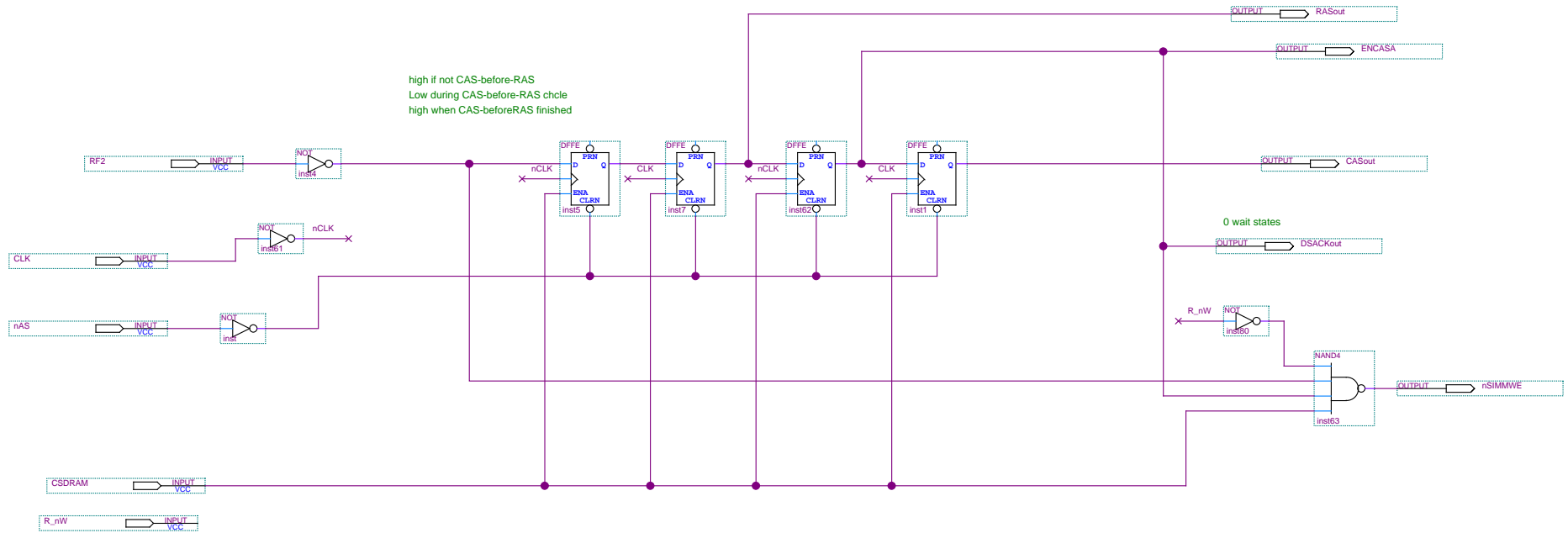
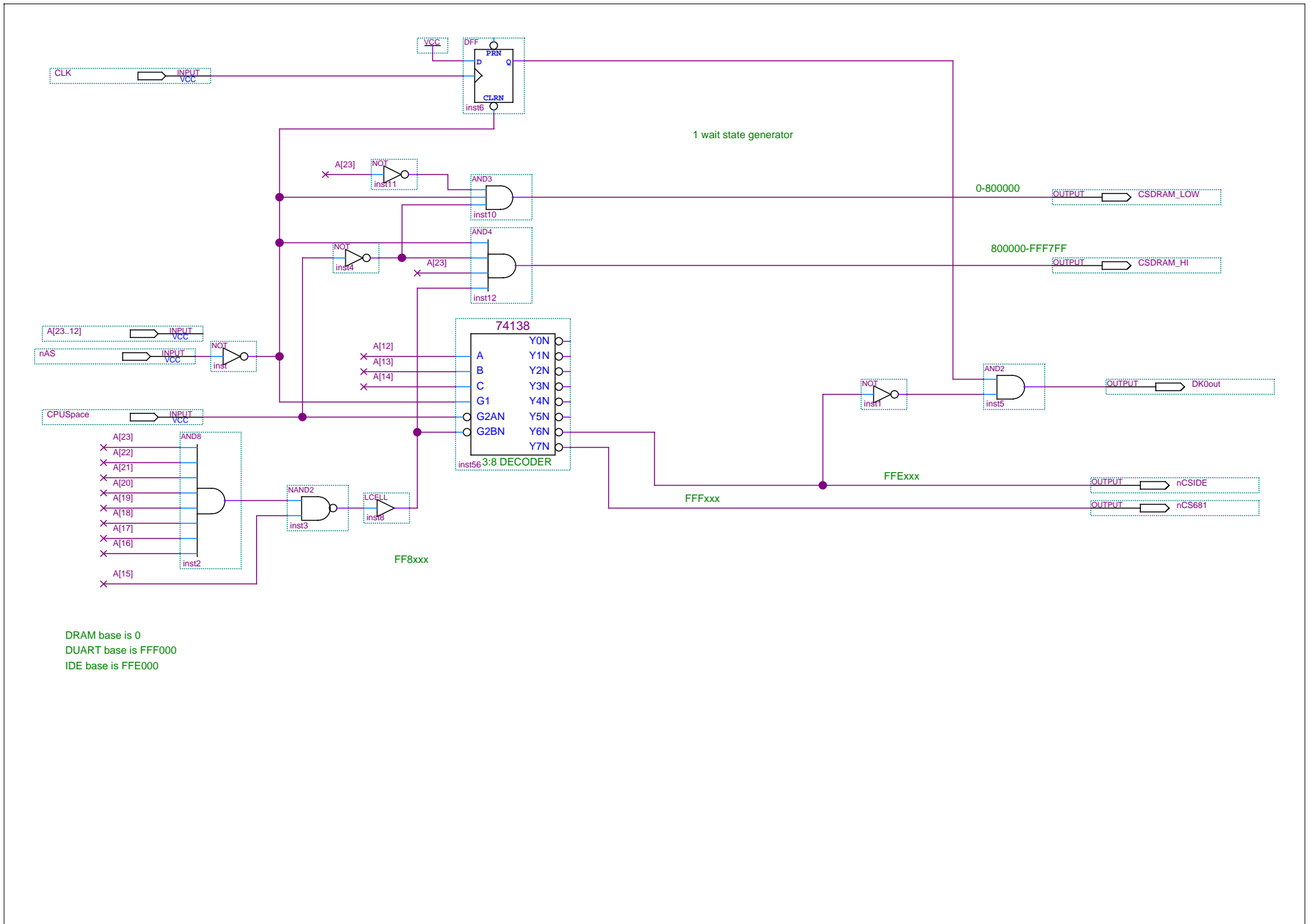


68000 timing is slower:
nAS is valid 3/35 after rising edge of S2, plenty of setup time for address valid at that time.
Assert RAS at falling edge of S2, then assert CAS at falling edge of S4

high if not CAS-before-RAS
Low during CAS-before-RAS chcle
high when CAS-beforeRAS finished





DRAM base is 0
DUART base is FFF000
IDE base is FFE000

