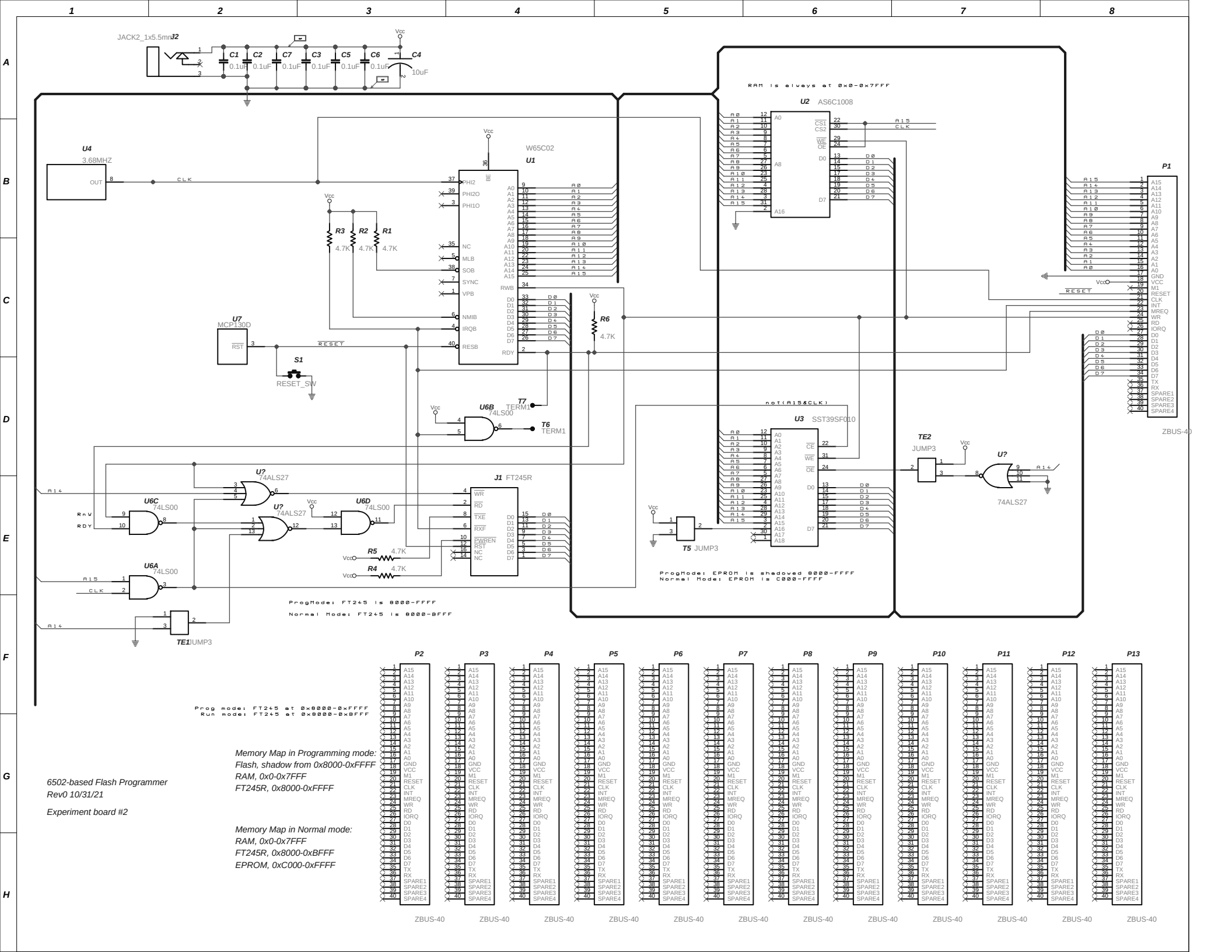
JACK2_1x5.5mm J2
C1 0.1uF
C2 0.1uF
C7 0.1uF
C3 0.1uF
C5 0.1uF
C6 0.1uF
C4 10uF
Vcc
U4 3.68MHZ
OUT
CLK
W65C02
U1
PHI2
PHI2O
PHI1O
NC
MLB
SOB
SYNC
VPB
NMIB
IRQB
RESB
RWB
RDY
R3 4.7K
R2 4.7K
R1 4.7K
R6 4.7K
U7 MCP130D
RST
RESET
S1
RESET_SW
U6B 74LS00
T7 TERM1
T6 TERM1
RAM is always at 0x0-0x7FFF
U2 AS6C1008
CS1
CS2
WE
OE
A16
P1
ZBUS-40
not(A15&CLK)
U3 SST39SF010
A18
TE2 JUMP3
U? 74ALS27
A14
T5 JUMP3
ProgMode: EPROM is shadowed 8000-FFFF
Normal Mode: EPROM is C000-FFFF
U6C 74LS00
U6D 74LS00
U6A 74LS00
RnV
RDY
CLK
A15
J1 FT245R
WR
RD
TXE
RXF
PWREN
RST
R5 4.7K
R4 4.7K
ProgMode: FT245 is 8000-FFFF
Normal Mode: FT245 is 8000-BFFF
TE1 JUMP3
P2
P3
P4
P5
P6
P7
P8
P9
P10
P11
P12
P13
Prog mode: FT245 at 0x8000-0xFFFF
Run mode: FT245 at 0x8000-0xBFFF
Memory Map in Programming mode:
Flash, shadow from 0x8000-0xFFFF
RAM, 0x0-0x7FFF
FT245R, 0x8000-0xFFFF
Memory Map in Normal mode:
RAM, 0x0-0x7FFF
FT245R, 0x8000-0xBFFF
EPROM, 0xC000-0xFFFF
6502-based Flash Programmer
Rev0 10/31/21
Experiment board #2