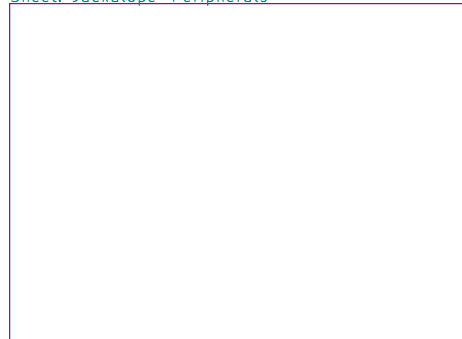


Sheet: [Jackalope-CPU Memory](#)



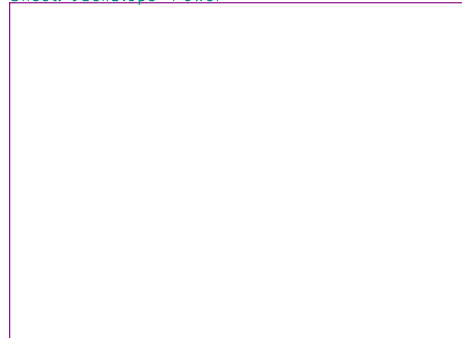
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Sheet: [Jackalope-Peripherals](#)



File: Jackalope-Peripherals.sch

Sheet: [Jackalope-Power](#)



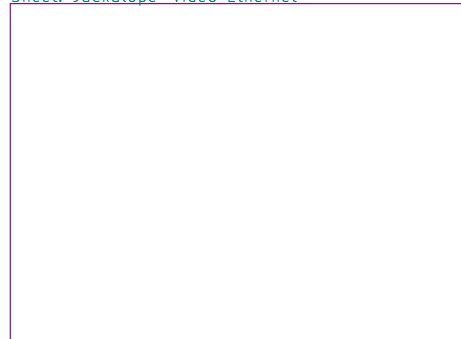
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Sheet: [Jackalope-Glue Logic](#)



File: Jackalope-Glue Logic.sch

Sheet: [Jackalope-Video Ethernet](#)



File: Jackalope-Video Ethernet.sch

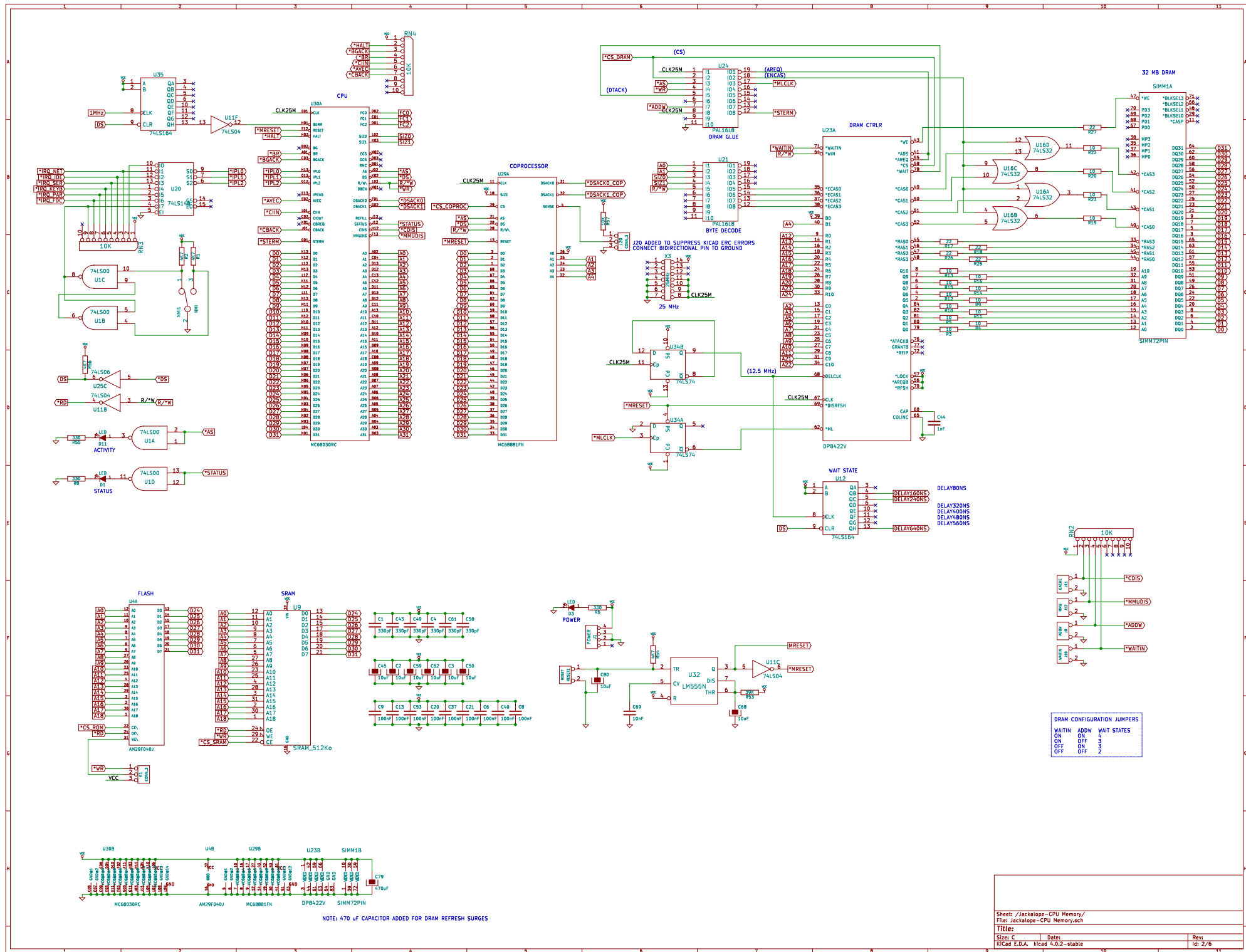
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Title:

Size: A4
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Date:

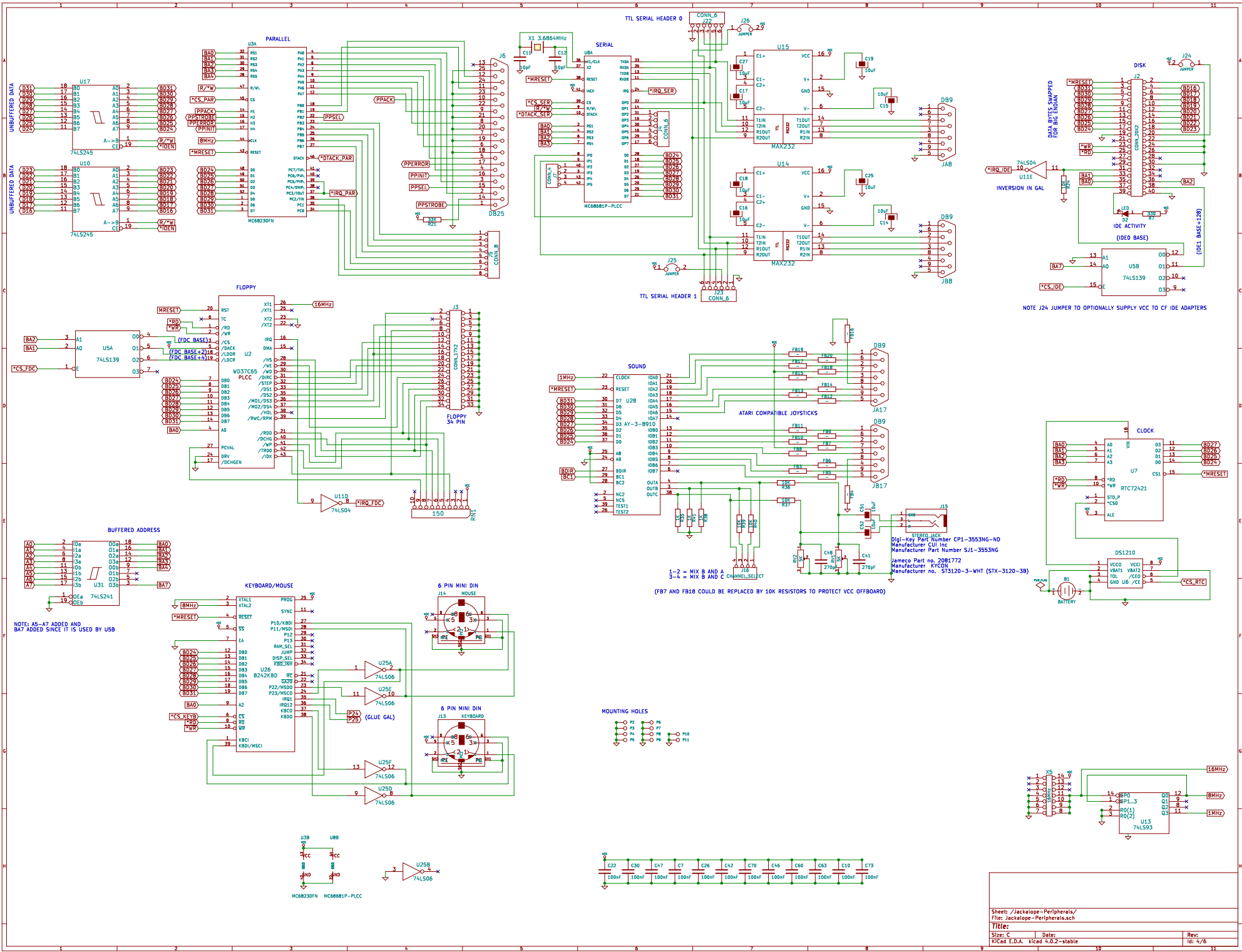
Rev:
Id: 1/6



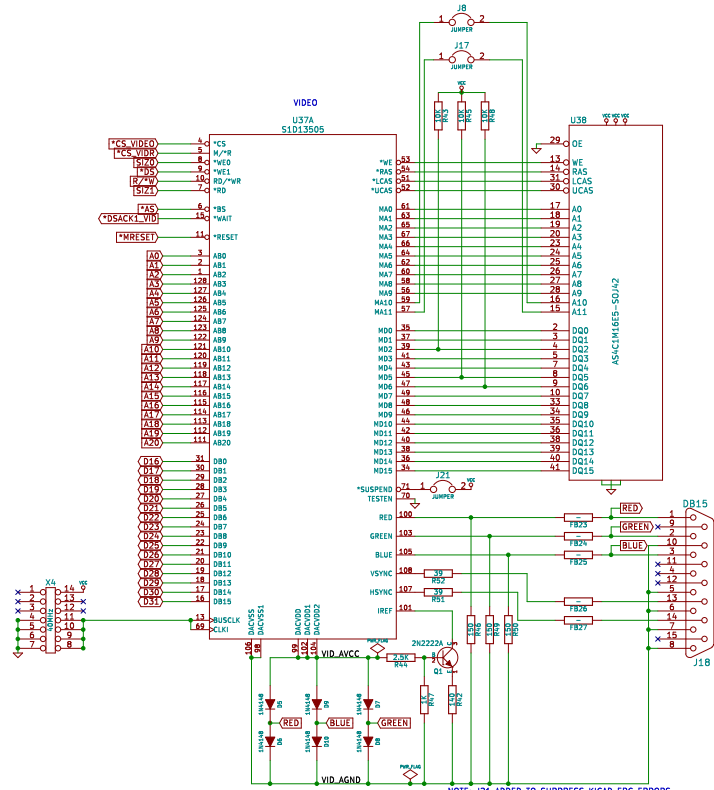
NOTE: 470 pF OF CAPACITOR ADDED FOR DRAM REFRESH SURGES

Sheet: /Jackalope-CPU Memory/
 File: Jackalope-CPU Memory.sch
 Title:
 Size: C Date:
 Picked E.D.A. Kicad 4.0.2-stable Rev:
 Id: 2/6

WAITIN	ADDV	WAIT STATES
ON	ON	4
ON	OFF	3
OFF	ON	3
OFF	OFF	3

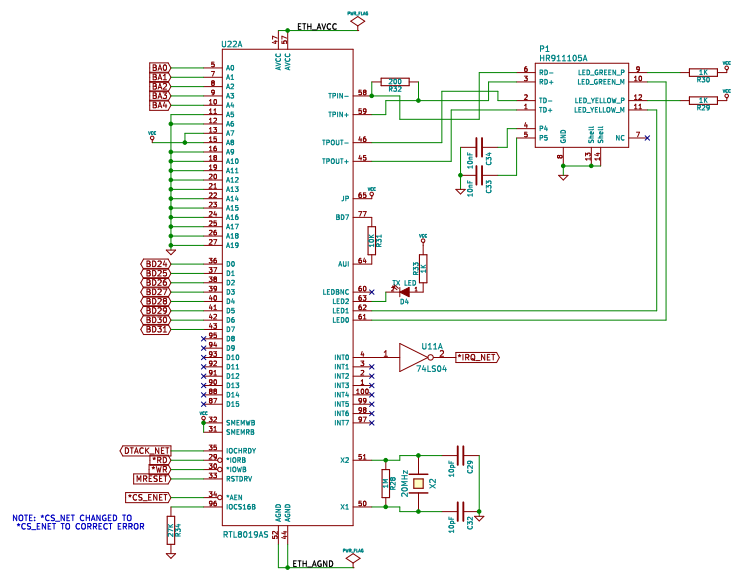
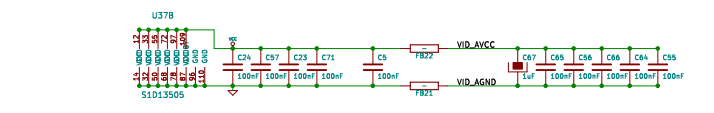


NOTE: J8 AND J17 ARE PRE-POSITIONED JUMPERS FOR POTENTIAL 1M X 16 VIDEO DRAM EXPANSION. PER ORIGINAL DESIGN ONLY UTILIZED 256K X 16 DRAM. USE SOLDERED WIRE JUMPERS TO CONNECT ADDRESS LINES.

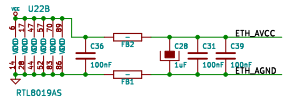


NOTE: D5-D10 ARE REVERSED COMPARED TO EPSON DATASHEET PAGE *03

NOTE: J21 ADDED TO SUPPRESS KICAD ERC ERRORS. CONNECT BIDIRECTIONAL PIN TO VCC. USE SOLDERED WIRE JUMPER TO CONNECT LINES.



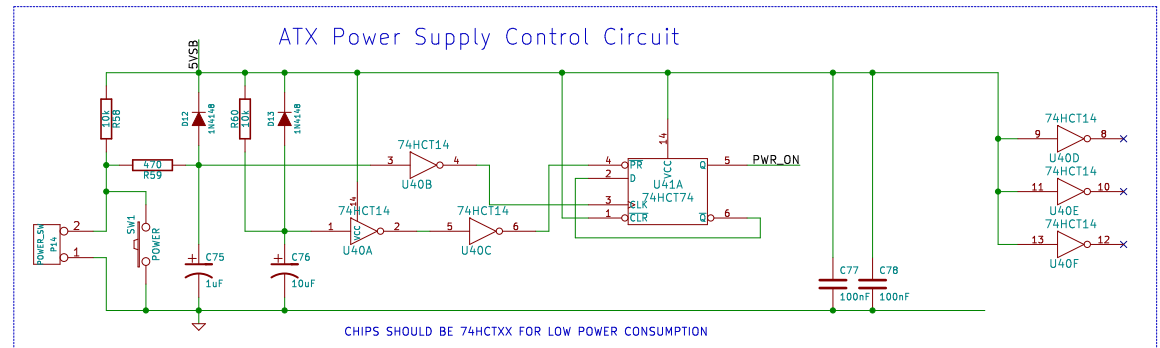
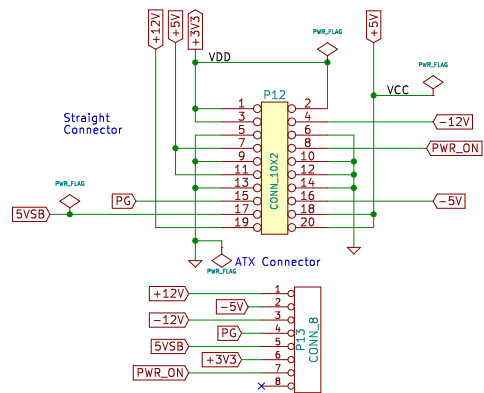
NOTE: *CS_ENET CHANGED TO *CS_ENET TO CORRECT ERROR



S1D13505 CONFIGURATION

VALUE	MD
0	0
0	1
0	2
0	3
0	4
1	5
1	6
0	7
0	8
0	9
0	10
0	11
0	12

0 = INTERNAL PULLDOWN



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NBVM Users Group

Sheet: /Jackalope-Power/
 File: Jackalope-Power.sch

Title: 6809 and 6502 ATX and 6U

Size: B Date: 19 feb 2014

Rev: 012

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Id: 6/6